



05-16-04 2124  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Choo et al.

Serial No.: 09/863,650

Group Art Unit: 2124

Filed: May 22, 2001

Examiner: Chuong D. Ngo

For: APPARATUS AND METHOD FOR  
IMPLEMENTING EFFICIENT  
ARITHMETIC CIRCUITS IN  
PROGRAMMABLE LOGIC DEVICES

Attorney Docket No.: 060889-0038  
(formerly 9818-038-999)

RESPONSE TO RESTRICTION REQUIREMENT MAILED APRIL 13, 2004

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

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MAY 13 2004

Technology Center 2100

Sir:

In response to the Restriction Requirement mailed April 13, 2004, in connection with the above-identified application, applicant elects for prosecution Species II, directed to an invention corresponding to Figure 2B. Claims 4-10 and 23-26 are readable on Species II.

Respectfully submitted,

Date May 6, 2004

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